

**Amendments to the Claims**

1. (CURRENTLY AMENDED) A bus system ~~(100, 300)~~ comprising: a bus that facilitates communication of signals among a plurality of devices, an access control matrix ~~(160)~~ that identifies access rights between one or more initiators ~~(110)~~ and one or more targets ~~(120)~~ of the plurality of devices, and an access controller ~~(140, 310)~~, operably coupled to the bus and to the access control matrix ~~(160)~~, that is configured to: monitor the bus for a data transfer request from a select initiator of the one or more initiators ~~(110)~~ to a select target of the one or more targets ~~(120)~~, and selectively inhibit a data transfer between the select initiator and the select target based on the access rights between the select initiator and the select target.

2. (CURRENTLY AMENDED) The bus system ~~(100, 300)~~ of claim 1, wherein the access controller ~~(140, 310)~~ is operably coupled in series between the one or more initiators ~~(110)~~ and the one or more targets ~~(120)~~.

3. (CURRENTLY AMENDED) The bus system ~~(100)~~ of claim 2, wherein each of the one or more initiators ~~(110)~~ are independently coupled to the access controller ~~(140)~~ via an input port of a plurality of input ports of the access controller ~~(140)~~, and the access control matrix ~~(160)~~ identifies the access rights between the one or more initiators ~~(110)~~ and the one or more targets ~~(120)~~ based on the input port corresponding to each of the one or more initiators ~~(110)~~.

4. (CURRENTLY AMENDED) The bus system ~~(100, 300)~~ of claim 1, wherein the access controller ~~(140, 310)~~ provides an enabling signal to each of the one or more targets ~~(120)~~, and selectively inhibits the data transfer between the select initiator and the select target via a control of the enabling signal associated with the select target.

5. (CURRENTLY AMENDED) The bus system ~~(300)~~ of claim 1, wherein the access controller ~~(310)~~ is operably coupled in parallel with the one or more initiators ~~(110)~~ and the one or more targets ~~(120)~~.

6. (CURRENTLY AMENDED) The bus system ~~(300)~~ of claim 5, wherein the access controller ~~(310)~~ selectively inhibits the data transfer between the select initiator and the select target by asserting a signal state onto the bus that prevents the select initiator from changing the signal state on the bus.

7. (CURRENTLY AMENDED) The bus system ~~(100, 300)~~ of claim 1, wherein the access control matrix ~~(160)~~ is configured to identify the access rights between the one or more initiators ~~(110)~~ and the one or more targets ~~(120)~~ based on at least one of: an initiator classification and a target classification.

8. (CURRENTLY AMENDED) An electronic system ~~(100, 300)~~ comprising: a plurality of devices ~~(110, 120)~~ that are configured to communicate signals among each other, an access control matrix ~~(160)~~ that identifies access rights between pairs of devices of the plurality of devices ~~(110, 120)~~, and an access controller ~~(140, 310)~~, operably coupled to the plurality of devices ~~(110, 120)~~ and to the access control matrix ~~(160)~~, that is configured to: receive a data transfer request from a first device of the plurality of devices ~~(110, 120)~~ for effecting a data transfer with a second device of the plurality of devices ~~(110, 120)~~, and selectively inhibit the data transfer between the first device and the second device, based on the access rights between the pair of devices ~~(110, 120)~~ corresponding to the first device and the second device.

9. (CURRENTLY AMENDED) The electronic system ~~(100)~~ of claim 8, wherein the access controller ~~(140)~~ is operably coupled in series between the first device and the second device.

10. (CURRENTLY AMENDED) The electronic system ~~(100)~~ of claim 9, wherein the first device is coupled to the access controller ~~(140)~~ via an input port of a plurality of input ports of the access controller ~~(140)~~, and the access control matrix ~~(160)~~ identifies the access rights between the first device and the second device based on the input port corresponding to the first device.

11. (CURRENTLY AMENDED) The electronic system ~~(100, 300)~~ of claim 8, wherein the access controller ~~(140, 310)~~ inhibits the data transfer between the first

device and the second device via control of an enabling signal associated with the second device.

12. (CURRENTLY AMENDED) The electronic system ~~(300)~~ of claim 8, wherein the access controller ~~(310)~~ is operably coupled in parallel with the first device and the second device via a bus that is used to communicate the signals between the first device and the second device.

13. (CURRENTLY AMENDED) The electronic system ~~(100, 300)~~ of claim 12, wherein the access controller ~~(140, 310)~~ selectively inhibits the data transfer between the first device and the second device by asserting a signal state onto the bus that prevents the first device from changing the signal state on the bus.

14. (CURRENTLY AMENDED) The electronic system ~~(100, 300)~~ of claim 8, wherein the access control matrix ~~(160)~~ is configured to identify the access rights between the pairs of devices ~~(110, 120)~~ based on a classification of one or more of the plurality of devices ~~(110, 120)~~.

15. (CURRENTLY AMENDED) The electronic system ~~(100, 300)~~ of claim 8, wherein the plurality of devices ~~(110, 120)~~ include at least one of: a video processing device, a user-identification device, a security device, a memory device, and a processing device.

16. (CURRENTLY AMENDED) A method of controlling access to a plurality of targets ~~(120)~~, including determining an identification of an initiator ~~(110)~~ that is attempting to access a select target ~~(120)~~ of the plurality of targets, determining a predefined access right between the initiator ~~(110)~~ and the select target ~~(120)~~, based on the identification of the initiator ~~(110)~~, and selectively inhibiting access to the select target ~~(120)~~, based on the predefined access right between the initiator ~~(110)~~ and the select target ~~(120)~~.

17. (CURRENTLY AMENDED) The method of claim 16, wherein selectively inhibiting the access to the select target ~~(120)~~ includes controlling an enabling signal of the select target ~~(120)~~.

18. (CURRENTLY AMENDED) The method of claim 16, wherein selectively inhibiting the access to the select target ~~(+20)~~ includes asserting a signal state onto a bus that prevents the initiator ~~(+10)~~ from changing the signal state on the bus.

19. (CURRENTLY AMENDED) The method of claim 16, wherein determining the predefined access right between the initiator ~~(+10)~~ and the select target ~~(+20)~~ is based on at least one of: a classification associated with the initiator ~~(+10)~~, and a classification associated with the select target ~~(+20)~~.